

PhD Subject / CIFRE contract

SLA-aware MicroServices Architecture programming on fault-tolerant distributed systems

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Re-engineering a high frequency e-payment authorization system

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Context

HPS/Acqualife is a leading world-wide company for supporting electronic payments. HPS PowerCARD product is a robust, open, evolutive solution to handle (i.e. to authorize) electronic payment transactions originating from various terminals, e.g; ATM, mobile, internet: the key PowerCARD operation is to decide to authorize or not a transaction in close to real-time. Authorization is decided by querying both international (Visa, Mastercard, Amex, ...), domestic or specific host banking institutions systems and PowerCARD internal data warehouse via SQL stored procedures, and apply specific controls about the requested transaction. The volume of transaction authorizations to handle is of a handful of thousands per second. Next generation of PowerCARD, *PowerCARD Switch* is under specification. It will feature a microservices based architecture (MSA), with the aim to benefit from private cloud hosting, scalability and resilience. The general goal of the industrial PhD is to contribute innovative ideas and solutions in this context.

SCALE is a joint team between CNRS, and Univ. of Nice Sophia Antipolis/Université Côte d'Azur <https://scale-project.github.io/> and is a continuation of the former Equipe Projet INRIA [OASIS](#). It builds fundamental principles, techniques, and tools allowing implementation, analysis, and verification of reliable and efficient large-scale distributed solutions. Its activities span from formal methods and programming models (actor based innovative models) to practical implementation of applications and middleware. SCALE expertise on distributed programming models, design and implementation of run-time supports for these models, as well as deep knowledge and optimization of third-party solutions (like distributed stream processing systems as Apache STORM) will be crucial for the success of this PhD thesis. The Scale team has also a strong expertise in the design and implementation of applications to run on top of such homemade or third-party middleware, for illustration or benchmarking purposes.

Subject

Successful and smooth migration of multi-tiers and sometimes monolithic architectures to Micro Services Architectures (MSA) is becoming a hot topic in the industry. Success relies on intelligent use of mixture of cutting-edge framework stacks (eg SMACK/Spark-Mesos-Akka-Cassandra-Kafka); from

the application viewpoint the main difficulty, that we want to address as a central research question during this PhD, is the way the micro services should be composed to support specific flows of business operations while ensuring the expected performance level, as the main criteria is to reply to a requested authorization in the given delay. Meeting the availability and expected performance level in any situation, including if the distributed system gets partitioned for any reason, requires supporting vertical and horizontal scalability; but, at the same time, due to the well-known CAP theorem *PowerCARD Switch* will have to incorporate specific solutions to reconcile service replicas data in the background.

Indeed, in this specific context of being able to respond in real-time to thousands of payment authorizations per second, the flows of *Power CARD* should feature a high degree of plasticity/adaptability resp. at design, and at runtime. Not only the set of supported flows can be very different from one deployment to another, but also new transaction authorization process can popup, so the system should be highly configurable even once deployed and running. Because of high variability both in the volume and nature of the authorizations to process, it is required to execute on a distributed thus prone to failures system. All such needs call for a resilient programming model. More precisely, the core of the PhD work will be to research an appropriate programming model and associated supporting middleware support, that may incorporate auto-adaptable capabilities to meet the SLA of flows. This of course calls for a reactive programming of the flows (eg reactive workflows [21]) along both data-driven (each transaction is a data to process) and event-driven (asynchronous/reactive property) perspectives, which is recognized to be tricky to play with particularly in case of failures [20]; moreover, we want to envision to introduce self-adaptation of the programmed flows in order for them to better support parallel or alternative paths, or to self-optimize the way they handle streams of transactions (eg. one by one, or in batch, but what batch size in this case). Not only self-adaptation of the data-driven program is needed but this is also important that the supporting middleware (as the message bus) features auto-adaptation capabilities (as in [14]) and that the multi-levels of monitoring and control do interact and collaborate.

In [15] authors mention that reports about experience of migrating a legacy system to a new MSA-based architecture are rare; alas it could be valuable to push further this innovative approach from academia to industry. Consequently, by the end of the PhD, it is also our intention (as in [17]) to publish lessons learned about the *PowerCARD* system migration process. The particularity we foreseen is that the system to migrate has some peculiar and strong non-functional expectations and SLAs to fulfill, so we believe that the way they will be fulfilled can be valuable in similar mission-critical contexts.

To summarize, the goal of this PhD research work is to contribute to the design and development of *PowerCARD* next generation central module in charge of the processing of payment authorizations. The module designed along a MSA will exhibit self-capabilities, at all levels.

Work environment, contact, process of application

The work will primarily take place in HPS Worldwide/ ACPQUALIFE located in Aix-en-Provence, under local supervision of Gael Migliorini for HPS and the academic supervision of Pr F. Baude, and Dr F. Huet CNRS/UNS I3S laboratory in Sophia-Antipolis for the Doctoral School. The PhD student will be hired by HPS Worldwide/ ACPQUALIFE.

The student will be registered at the Université Côte d'Azur, Ecole doctorale EDSTIC <http://edstic.unice.fr/fr>

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The position will be open until filled, interested candidates are invited to send their application as soon as possible. The start of the PhD is expected in Fall 2019. Interested candidates are encouraged to contact the supervisors mentioned above if they have any question, and invited to send the following documents to them:

- a detailed CV,
- a list of courses and grades during the MSc (and if possible earlier years),
- a list of 2-3 references willing to support their application,
- a short statement of interest and any other information useful to evaluate the application.

Relevant References of the team

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