A cost model for green fog computing and networking

Ramon Aparicio-Pardo¹, Lucile Sassatelli²,

¹Université Côte d’Azur, CNRS, I3S - Sophia Antipolis, France
E-mail: {rparicio,sassatelli}@i3s.unice.fr

ABSTRACT

5G services and 4K will stress even more the future access/aggregation networks, where video contents have already become the main traffic contributor. The deployment in the convergent access of local micro data-centers (DCs) nodes is one promising approach to successfully manage this challenge. These nodes will be responsible for switching enormous amounts of traffic and simultaneously performing heavy CPU tasks (such as video or radio base band processing). Only a flexible management of these nodes based on NFV, SDN and data analytics will enable to meet these tasks. In this paper, we present a consistent and complete cost model collecting main tradeoffs between energy savings and CPU processing suitable to be used in such a flexible management framework.

Keywords: FMC, C-RAN, power model, QoE model, video streaming, cloud/fog computing

1. INTRODUCTION

The video streaming share in the Internet traffic is growing at a blistering pace, representing 82% of all traffic by 2020 [1]. Moreover, a part of this growth is associated to the development of high bandwidth demanding applications, e.g. 360° videos (28Mbps [2]).

For access networks (cellular and fixed), this traffic surge is accompanied by a power consumption growth, motivating the need for improving the energy-efficiency of aggregation and access networks. One promising solution is the Fixed-Mobile Convergence (FMC) paradigm [3] (Fig.1 left). The idea behind FMC is to manage jointly the heterogeneous access technologies (e.g., optical, WiFi, 4G) in a unified fixed-mobile convergent optical network to consolidate within the cloud/fog fixed and mobile optical head-ends as well as the base station (base band) processing. The consolidation of the controllers and radio elements is known as Base Band Unit (BBU) hosting, an enabling key element of the FMC: the Cloud Radio Access Network (C-RAN) architecture [3].

The mutualized usage of physical resources (processing, networking equipment and power-consuming cooling infrastructure) allows significantly reducing power consumption. However, that requires fine control of network management to avoid performance losses. The key-enablers for this are Network Function Virtualization (NFV), Software Defined Networking (SDN), that allow fully exploiting the flexibility and scalability of cloud resources.

The management of these networks will have to account for video streaming as the new pivot of user traffic. Most Internet video streaming corresponds to so-called HTTP Adaptive bit rate Streaming (HAS), as the MPEG-DASH standard, that lets the video bit rate to be adapted over time to, e.g., best fit the available client bandwidth.

Augmented ISP nodes with IP routing, optical switching, computation and storage abilities will therefore be the elementary bricks of future ISP tele networks. Based on a thorough literature study, we propose a complete cost model, necessary for any operational design, collecting in a comprehensive way: power, bandwidth, computational costs and video quality.

2. NODE ARCHITECTURE

The micro-DC node architecture, depicted in Fig. 1 right, is the conventional 2-(or 3)-tier for DC [4]-[5] (a 2-tier here), with additional optical and electronic networking equipment. We emphasize that the network nodes in Fig.1 left are these micro-DCs within the ISP network implemented with the hardware in Table 1. The elementary blocks of the node are:

1) **DC servers**, which are responsible for carrying out: (i) the video transcoding operations and (ii) the radio baseband operations performed in the Base Band Unit (BBU) at the Base Stations (BSs) in a today’s RAN.

2) **Electronic networking equipment**, which are the Ethernet gigaswitches, interconnecting the servers (first aggregation level of the 2-tier data center); and, the **IP edge routers**, interconnecting the Ethernet gigaswitches (second aggregation level) and providing access to the Internet.

3) **Optical networking equipment**, which are the Optical Cross Connects (OXC) and the **OEO transponders** responsible for adding, dropping and switching the optical signals. We assume their power contribution as negligible [6].

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Model</th>
<th>Cores</th>
<th>CPU C(^{GHz})</th>
<th>RAM C(^{GB})</th>
<th>Switch. Capacity C(^{Gbps})</th>
<th>Idle Power P(^{idle})</th>
<th>Dyn. Power P(^{dyn})</th>
<th>Servers K</th>
</tr>
</thead>
<tbody>
<tr>
<td>Taurus DC server [7]</td>
<td>Intel Xeon e5-2630</td>
<td>2×6/12</td>
<td>2.3 GHz</td>
<td>32 GB</td>
<td>320 Gbps</td>
<td>95 W</td>
<td>125 W</td>
<td>16</td>
</tr>
</tbody>
</table>
3. POWER CONSUMPTION MODEL

The power consumption model of all the elements follows an affine function with two terms: a baseline power (idle power) and variable term depending on load. The baseline power usually accounts cooling, power supply and lightening systems.

3.1 Server Consumption Model

As in [7] the server power consumption is estimated as the sum of a baseline constant term (TDP) plus the dynamic consumption proportional to the number of active cores at the server. A whole core is activated when hosting a Virtual CPU (vCPU). We assume that (i) video transcoding and (ii) radio base band operations are performed by a Virtual Machine (VM) set \( VM \), where each \( vm \in VM \) requests \( d_{vm} \) vCPUs. The proportionality coefficient with respect to number of active cores is simply the quotient between the maximal dynamic power and the number of cores \( K \). Then, the total server consumption (static and dynamic) \( P_{CPU} \) is:

\[
P_{CPU} = P_{STAT} \cdot PUE + \frac{P_{DYN}}{K} \cdot \sum_{vm \in VM} d_{vm} = 106.4 + 10.417 \cdot \sum_{vm \in VM} d_{vm} \text{ Watts}
\]

where PUE is the Power Utilization Efficiency of the cooling equipment at the server chassis (1.12). Finally, we do not consider the storing consumption (e.g. for video caching) as typical values (a few Watts [8]) are negligible with respect to those shown in Table 1.

3.2 Electronic networking equipment

This networking equipment performs their switching operations in the electrical domain, constituting important sources of power consumption. We use the power model described in [5] with the values in Table 1. This model computes the overall consumption \( P_{ESW} \) of these devices following an affine function of the traffic load as:

\[
P_{ESW} = P_{STAT} + P_{DYN} \cdot f
\]

where \( f \) is the amount of Gbps traversing the device, \( P_{STAT} \) is the idle power, \( P_{DYN} \) is the difference between the maximal power consumption and the idle power (i.e. the extra power consumed if the device is fully load) and \( C_{ESW} \) is the maximal equipment capacity in Gbps.

However, values in Table 1 show that the idle power represents ca. 80% the total consumption, justifying to neglect the load dependency and overestimating the total power as the sum of idle and dynamic contributions, i.e. the maximal power consumption when the device is operating at \( C_{ESW} \) Gbps. Using the values in Table 1, the Ethernet gigaswitch power \( P_{EGS} \) and the IP edge router \( P_{IPR} \) power can be computed as:

\[
P_{EGS} = P_{STAT} + P_{DYN} = 1614 + 404 = 2020 \text{ Watts}
\]

\[
P_{IPR} = P_{STAT} + P_{DYN} = 3640 + 910 = 4550 \text{ Watts}
\]

3.3 Optical switching equipment

The optical switching equipment consists of OXCs and OEO transponders. The formers are responsible for switching a wavelength channel from an incoming to an outgoing fiber without traversing the electrical domain, i.e. without requiring CPU processing, yielding to much less consumption than their electrical counterpart. We use the model in [6], assuming 80 WDM channels per fiber, where power \( P_{OXC} \) linearly depends on the fiber ports as:

\[
P_{OXC} = 150 + 100 \cdot a + 85 \cdot d \text{ Watts}
\]
where $a$ is the number of add/drop ports and $d$ is the number of network-side ports. Considering (i) the access/aggregation network as a multi-stage tree topology ($d=3, a=2$) and (ii) optical signal are only added/dropped at the end-nodes or at the intermediate nodes performing processing, power $P_{\text{OXC}}$ can be computed as

$$P_{\text{OXC}} = \begin{cases} 
405 \text{ Watts}, 
& \text{if node transparently bypasses optical signals} \\
605 \text{ Watts}, 
& \text{if node performs electrical (BBU or video) processing}
\end{cases}$$

Assuming a OEO transponder ([6]) as a (non-coherent) 10Gbps Transponder/Muxponder, it consumes 40W [9]. Finally, it is worth noticing that these powers are negligible in comparison with the electrical switches powers.

### 4. BANDWIDTH MODEL FOR RADIO LAST HOP

Since BBU processing tasks are deported to a DC node in the aggregation network as depicted in Fig. 1, traffic coming from the mobile edges is transported over the optical fiber plant using a digital Radio Over Fiber (RoF) interface (typically, CPRI [14]). This section presents the model assumptions about the wireless last hop.

We consider that each mobile edge node in Fig 1 is a BS collecting the traffic of a single macro cell site and providing LTE radio coverage of 3 sectors with 20 MHz bandwidth and 2×2 MIMO configuration. Table 2 shows the detailed cell site configuration. For this configuration and for the typical LTE values [11], the bit rate per RB can be computed as in [12] obtaining a value of 759.36 kbps. Since our cell site uses 6 antennas (3 sectors with 2×2 MIMO) and 100 RBs (20 MHz bandwidth) [13], the maximal aggregated rate at a mobile edge is $C_{\text{LTE}} = 455.62 \text{ Mbps} \times (6 \times 100) = 75.936 \text{ Mbps}$.

The LTE traffic is transported between the mobile end node and the deported BBU by CPRI. That implies to transform the radio band signals from analog to digital, entailing CPRI bit rate $C_{\text{CPRI}}$ higher than $C_{\text{LTE}}$. Assuming CPRI standard sampling frequency of 30.72 MHz [13], and sample bit width of 15 [14], the CPRI bit rate (I/Q sample data rate) per antenna is 0.9216 Gbps ($2 \times 15 \times 30.72 \text{ MHz}$). Then for our cell site (6 antennas), the maximal aggregated CPRI rate is 5.53 Gbps ($6 \times 0.9216 \text{ Gbps}$) carried in the CPRI option 7 ($C_{\text{CPRI}} = 9.83 \text{ Gbps}$) [14].

<table>
<thead>
<tr>
<th># Sectors</th>
<th>LTE Bandwidth</th>
<th>MIMO configuration</th>
<th>Freq. domain usage</th>
<th>Code Rate</th>
<th>Spectral efficiency</th>
<th>Quantization</th>
<th>$C_{\text{CPRI}}$</th>
<th>$C_{\text{LTE}}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>20 MHz</td>
<td>2×2 MIMO</td>
<td>Full load</td>
<td>13</td>
<td>64 QAM</td>
<td>0.75</td>
<td>4523.43 bps/Hz</td>
<td>24 bit</td>
</tr>
</tbody>
</table>

### 5. CPU PROCESSING MODEL

The physical servers at the micro-DC nodes of the convergent access perform either Digital Baseband preprocessing or Video Transcoding tasks. This section introduces the model used to estimate the CPU needs.

#### 5.1 Digital baseband processing

Baseband processing is carried out at the micro DCs by the VMs (also called Virtual Digital Units, VDU) for the BBU units deported from the cell BSs. We use the model in [10] to find the number of GOPS per BS, which is divided into subcomponents of the digital base band processing equipment whose number of GOPS depends on: bandwidth, spectral efficiency, number of antennas, system load (frequency-domain), number of spatial streams and quantization. Then, the overall number of GOPS is simply computed as the sum for all the digital subcomponent GOPS. For our cell site scenario (Table 2), the overall number of GOPS is 229.2813. From the distributed computing projects Asteroids@home² and SETI@home³, we find the GOPS per core used by our servers (Table 1) when signal processing is performed. This figure is close to 2.5 GOPS per core (or vCPU) in both projects, whereby a value of $d=91.713$ vCPUs required by the digital baseband processing of each BS.

#### 5.2 Video Transcoding

Transcoding changes the encoding parameters of a video (usually rate and resolution). In this work, we assume that the VMs performing the transcoding tasks at the micro-DCs generates continuous video rates from one unique high quality video version cached at the DC (in this work 1080p and 4K versions). In contrast to standard adaptive streaming (as MPEG-DASH) where versions encoded at certain (discrete) different rates are stored, continuous video rate adaptation enables higher QoE at the client at less storing overheads [15], [16].

From [17], the transcoding effort is rather independent from the input and output coding rates and more sensitive to the resolutions. We consider 3 video types with increasing complexity (cartoon, movie and sport). Table 3 shows the number of pass-marks required to transcode a representation of one of the contents in Table 4 from 1080p to 1080, 720 or 360p and from 4K to 4K. These pass-marks values were computed from GHz values in [17], assuming the number of pass-marks for a 1080p transcoding as 2000⁴. The 4K resolution was not covered in [17], but from community.netgear⁵ forum, it is roughly estimated as four times the CPU effort for 1080p.

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² https://asteroidsathome.net/boinc/cpu_list.php
³ https://setiathome.berkeley.edu/cpu_list.php
⁵ https://community.netgear.com/t5/New-to-ReadyNAS/4k-transcoding/td-p/1097311
The required transcoding vCPUs can be found by dividing the values in Table 3 with the number of pass-marks per core used by our equipment (Table 1). According to cpubenchmark\textsuperscript{6} website, this figure is 8915/12 (742.9).

| Table 3. Transcoding CPU (pass-marks). |
|-----------------|-----------------|-----------------|
|                | Cartoon | Movie | Sport |
| 360p            | 620.44  | 985.40 | 912.41 |
| 720p            | 1007.30 | 1299.27| 1635.04 |
| 1080p           | 1136.89 | 1613.14| 2000  |
| 4K              | 4554.74 | 6452.55| 8000  |

| Table 4. Videos and corresponding type and resolutions. |
|----------------|-----------------|-----------------|
|                | Content Type | Video Name [19] | Video Name [20] |
|                | Cartoon | Movie | Sport |
| 360p            | Big Buck Bunny, Sintel Trailer | Bosphorus |
| 720p            | Old Town Cross | Jockey |
| 1080p           | Touchdown Pass, Rush Field Cuts | ReadySetGo |

6. QUALITY OF EXPERIENCE MODEL

We characterize the users’ Quality of Experience (QoE) model as a function depending on the coding parameters (rate and resolution) and on the video content, by means of the Video Quality Metric (VQM) [18], which is a full-reference metric better correlated to human perception than other similar metrics. We use some of the rate-distortion curves (VQM vs coding rate) presented in [19], Fig. 2 and [20], Fig. 21. The exact video sequences are shown in Table 4. Since smaller VQM scores correspond to better QoE values, we normalize these curves between 0 and 1, and we take as QoE function 1 – VQM score (0 represents the worst QoE; 1, the best). Then, we derive the QoE function by the same power law fitting as in [19], yielding a different concave function of the coding rate, for each resolution and video sequence. If a linear optimization model is targeted, a linear approximation of the QoE function may be considered as: \( QoE = ab + \beta, b \in [b_{\text{min}}, b_{\text{max}}] \). Table 5 gives the parameters \( \alpha, \beta, b_{\text{min}} \) and \( b_{\text{max}} \) for each video and resolution. Recall that \( b \) is the nominal value in Mbps of the coding rate.

| Table 5. Linearization parameters: \( \alpha, \beta, b_{\text{min}}, b_{\text{max}} \). |
|----------------|-----------------|-----------------|
|                | Cartoon | Movie | Sport |
| 360p            | 0.1516  | 0.6305 | 0.2  | 0.7  | 0.3978 | 0.6796 | 0.2 | 1.7 | 0.4005 | 0.6801 | 0.2 | 2.2 |
| 720p            | 0.4668  | 0.6934 | 0.2  | 1.7  | 0.3421 | 0.8395 | 0.7 | 3.2 | 0.1216 | 0.8581 | 0.7 | 7.2 |
| 1080p           | 0.3456  | 0.6693 | 0.2  | 2.77 | 0.1336 | 0.7603 | 1.2 | 6.2 | 0.1338 | 0.7486 | 1.2 | 8.2 |
| 4K              | 0.0499  | 0.1098 | 2.2 | 22.7 | 0.0466 | 0.1025 | 2.2 | 24.2 | 0.0443 | 0.0975 | 2.2 | 24.7 |

7. CONCLUSIONS

The exhaustive micro DC model presented in this paper is suitable to be used in future works investigating the beneficial tradeoff of co-locating video and BBU processing in 5G Cloud-RAN, as suggested in [21].

REFERENCES


\textsuperscript{6} http://www.cpubenchmark.net